

Application Number

IDS Flag Clearance for Application

**IDS  
Information**

Content	Mailroom Date	Entry Number	IDS Review	Reviewer
M844	08-01-2005	32	<input checked="" type="checkbox"/>	08-09-2005 16:16:49 gstanley
M844	08-31-2005	37	<input checked="" type="checkbox"/>	10-05-2005 13:19:54 WLouie
M844	10-07-2005	42	<input checked="" type="checkbox"/>	10-12-2005 15:11:41 mbrunson
M844	11-09-2005	45	<input checked="" type="checkbox"/>	11-14-2005 10:05:56 lspruell
M844	01-03-2006	47	<input checked="" type="checkbox"/>	01-12-2006 11:26:02 ajones1
M844	10-18-2004	23	<input checked="" type="checkbox"/>	02-12-2005 10:59:31 vgrayshort
M844	01-19-2005	26	<input checked="" type="checkbox"/>	02-12-2005 11:00:16 vgrayshort
M844	03-23-2005	30	<input checked="" type="checkbox"/>	04-08-2005 19:36:20 yrobinson

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	146680	("light emitting device" "light emitting diode")	US-PGPUB; USPAT; JPO; DERWENT	2006/04/17 14:39
2	BRS	L2	3904	L1 and (heterostructure heterojunction "multi-layer stack" "stacked structure" "stack structure" "light-emitting structure")	US-PGPUB; USPAT; EPO; DERWENT	2006/04/17 14:56
3	BRS	L5	132	2 and (dielectric insulation protective passivation) near5 (patterned "light diffusing" "light diffusion" "light spatial" "light scattering")	US-PGPUB; USPAT; EPO; DERWENT	2006/04/17 14:56
4	BRS	L6	77	5 and @ad<"20030415"	US-PGPUB; USPAT; EPO; DERWENT	2006/04/17 14:56
5	IS&R	L7	8222	(257/79,94,95,98,99,100,103).CC LS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/04/17 14:56
6	BRS	L8	1356	7 and (heterostructure heterojunction "multi-layer stack" "stacked structure" "stack structure" "light-emitting structure")	US-PGPUB; USPAT; EPO; DERWENT	2006/04/17 14:56
7	BRS	L9	41	8 and (dielectric insulation protective passivation) near5 (patterned "light diffusing" "light diffusion" "light spatial" "light scattering")	US-PGPUB; USPAT; EPO; DERWENT	2006/04/17 14:56
8	BRS	L11	19	9 and @ad<"20030415"	US-PGPUB; USPAT; EPO; DERWENT	2006/04/17 15:00